

**AMENDMENTS TO THE CLAIMS:**

1. (Currently Amended) A solid-state imaging device comprising:  
  
a plurality of on-chip lenses each having a corresponding ~~to each~~ sensor portion in an imaging region;  
  
wherein ~~a center of reduction magnification of exit pupil correction which is performed for said plurality of on-chip lenses is set to a position~~ a primary lens registration location is selectively located at a region of the imaging device depending on a physical characteristic of the imaging element structure which is deviated from a center of said imaging region.
  
2. (Original) A solid-state imaging device according to claim 1, wherein a surface of said sensor portion or an opening of a light shielding film is in an asymmetrical shape.
  
3. (Currently Amended) A solid-state imaging device according to claim 1, wherein ~~the reduction magnification of~~ locations for said plurality of on-chip lenses is constant or is varied with respect to a distance from said center of the primary lens registration location ~~reduction magnification.~~
  
4. (Currently Amended) A solid-state imaging device according to claim 1, wherein locations for ~~the reduction magnification of~~ said plurality of on-chip lenses is varied continuously ~~or in a step shape~~ with respect to a distance from said center of the primary lens registration location ~~reduction magnification.~~